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EGR 119 Spring 2008
Test II
Michael R. Gustafson II

Name (please print)
In keeping with the Community Standard, I have neither provided nor received any assistance on this test. I understand if it is later determined that I gave or received assistance, I will be brought before the Undergraduate Judicial Board and, if found responsible for academic dishonesty or academic contempt, fail the class. I also understand that I am not allowed to speak to anyone except the instructor about any aspect of this test until the instructor announces it is allowed. I understand if it is later determined that I did speak to another person about the test before the instructor said it was allowed, I will be brought before the Undergraduate Judicial Board and, if found responsible for academic dishonesty or academic contempt, fail the class.

Signature:

## Instructions for Problem Sections

Please be sure to put each problem on its own page or pages - do not write answers to more than one problem on any piece of paper and do not use the back of a problem for work on a different problem. You will be turning in each of the problems independently.

Make sure that your name and NET ID are clearly written at the top of every page, just in case problem parts come loose in the shuffle. Make sure that the work you are submitting for an answer is clearly marked as such. Finally, when turning in the test, individually staple all the work for each problem and place each problem's work in the appropriate folder. Put this signature page as the "cover page" for the first problem.

Note that there may be people taking the test after you, so you are not allowed to talk about the test until I send along the OK. This includes talking about the specific problem types, how long it took you, how hard you thought it was - really anything. Please maintain the integrity of this test.

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## Problem I: [20 pts.] Controlled Sources

Given the following circuit:

and assuming that the values for the passive elements, sources $i_{\mathrm{s}}$ and $v_{\mathrm{p}}$, and constant $r$ are known,
(a) Clearly demonstrate the use of the Mesh Current Method in labeling unknowns for the circuit and in determining coupled linear algebra equations that could be used to solve for any currents in the system.
(b) Using the unknowns defined above, give expressions for the power absorbed by the controlled source and the power absorbed by the independent current source.

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## Problem II: [20 pts.] Passive Filter

Given the following circuit:

and assuming that the values for the passive elements are known, first redraw the circuit in the frequency domain, specifically using Laplace transforms. Then,
(a) Determine an expression for the transfer function:

$$
\mathbb{H}(s)=\frac{\mathbb{V}_{\text {out }}(s)}{\mathbb{V}_{\text {in }}(s)}
$$

(b) Re-cast the transfer function as a Fourier transform (i.e., $\mathbb{H}(j \omega)$ ). Then determine the value for the maximum magnitude of the transfer function, $H_{\max }$, and the frequency at which it occurs.
(c) What kind of filter is this?
(d) What is/are the half-power frequency/frequencies in terms of the passive element values?
(e) What is the step response $s_{\mathrm{r}}(t)$ of this filter (as a function of time)?

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## Problem III: [20 pts.] Active Filter

Given the following circuit:

and assuming that the values for the passive elements are known, first redraw the circuit in the frequency domain, specifically using Laplace transforms. Then,
(a) Determine an expression for the transfer function:

$$
\mathbb{H}(s)=\frac{\mathbb{V}_{\text {out }}(s)}{\mathbb{W}_{\text {in }}(s)}
$$

(b) Re-cast the transfer function as a Fourier transform (i.e., $\mathbb{H}(j \omega)$ ). Then determine the value for the maximum magnitude of the transfer function, $H_{\max }$, and the frequency at which it occurs.
(c) What kind of filter is this?
(d) What is the natural frequency of the response?
(e) What is the damping ratio of the response?
(f) The following depends on your answer regarding the filter type:
(a) If you believe this is a low-pass filter, determine the cutoff frequency and bandwidth.
(b) If you believe this is a band-pass filter, determine the center frequency and quality.
(c) If you believe this is a high-pass filter, determine the cutoff frequency and then wallow in self-pity, now secure in the knowledge that this is not a high-pass filter.

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## Problem IV: [20 pts.] Digital Logic

1. Given the following logical expression:

$$
\mathrm{E}=\overline{\mathrm{A}} \overline{\mathrm{C}} \overline{\mathrm{D}}+\mathrm{BD}+\mathrm{CD}
$$

complete the truth table and then generate a logic circuit that represents the expression using only one- or two-input gates. Be sure your gate shapes are very clear.

| A | B | C | D | E |
| :---: | :---: | :---: | :---: | :---: |
| 0 | 0 | 0 | 0 |  |
| 0 | 0 | 0 | 1 |  |
| 0 | 0 | 1 | 0 |  |
| 0 | 0 | 1 | 1 |  |
| 0 | 1 | 0 | 0 |  |
| 0 | 1 | 0 | 1 |  |
| 0 | 1 | 1 | 0 |  |
| 0 | 1 | 1 | 1 |  |
| 1 | 0 | 0 | 0 |  |
| 1 | 0 | 0 | 1 |  |
| 1 | 0 | 1 | 0 |  |
| 1 | 0 | 1 | 1 |  |
| 1 | 1 | 0 | 0 |  |
| 1 | 1 | 0 | 1 |  |
| 1 | 1 | 1 | 0 |  |
| 1 | 1 | 1 | 1 |  |

2. Given the logical circuit below, write out logical expressions for each of the three outputs and then complete the truth table. Note - the expressions are most easily written by interpreting the circuit; writing the expressions based on the truth table will give you many more and longer terms than are necessary.


| A | B | C | D | E | F | G |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 0 | 0 | 0 | 0 |  |  |  |
| 0 | 0 | 0 | 1 |  |  |  |
| 0 | 0 | 1 | 0 |  |  |  |
| 0 | 0 | 1 | 1 |  |  |  |
| 0 | 1 | 0 | 0 |  |  |  |
| 0 | 1 | 0 | 1 |  |  |  |
| 0 | 1 | 1 | 0 |  |  |  |
| 0 | 1 | 1 | 1 |  |  |  |
| 1 | 0 | 0 | 0 |  |  |  |
| 1 | 0 | 0 | 1 |  |  |  |
| 1 | 0 | 1 | 0 |  |  |  |
| 1 | 0 | 1 | 1 |  |  |  |
| 1 | 1 | 0 | 0 |  |  |  |
| 1 | 1 | 0 | 1 |  |  |  |
| 1 | 1 | 1 | 0 |  |  |  |
| 1 | 1 | 1 | 1 |  |  |  |

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## Problem V: [20 pts.] A/D D/A Sampling Aliasing Quantization

Make sure your calculator is set to radians. The signal:

$$
x(t)=\cos (2 \pi t)+\sin (12 \pi t)
$$

is sampled every 0.2 sec , starting at 0 sec and lasting for one second, yielding a total of 6 samples as shown:


A 3-bit flash A/D converter with a range from -2 V to 2 V is used to sample the signal.

1. For each of the six samples taken, what is the binary representation of the value sampled?
2. For each of the six samples taken, what voltage does the binary representation translate to?
3. What is the quantization size?
4. Is there aliasing? Why or why not?
