

Duke University
Edmund T. Pratt, Jr. School of Engineering

ECE 110L Fall 2016
Test I
Michael R. Gustafson II

Name and NetID (please print)_____

In keeping with the Community Standard, I have neither provided nor received any assistance on this test. I understand if it is later determined that I gave or received assistance, I will be brought before the Undergraduate Conduct Board and, if found responsible for academic dishonesty or academic contempt, fail the class. I also understand that I am not allowed to speak to anyone except the instructor about any aspect of this test until the instructor announces it is allowed. I understand if it is later determined that I did speak to another person about the test before the instructor said it was allowed, I will be brought before the Undergraduate Conduct Board and, if found responsible for academic dishonesty or academic contempt, fail the class.

Signature:_____

Instructions

First - please turn **off** any cell phones or other annoyance-producing devices. Vibrate mode is not enough - your device needs to be in a mode where it will make no sounds during the course of the test, including the vibrate buzz or those acknowledging receipt of a text or voicemail.

Please be sure to put each problem on its own page or pages - do *not* write answers to more than one problem on any piece of paper and do not use the back of a problem for work on a *different* problem. You will be turning in each of the problems independently. This cover page should be stapled to the front of Problem 1.

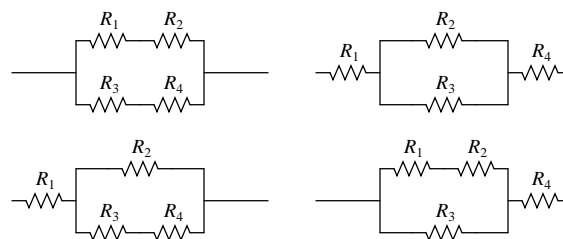
Make sure that your name *and* NetID are *clearly* written at the top of *every* page, just in case problem parts come loose in the shuffle. Make sure that the work you are submitting for an answer is clearly marked as such. Finally, when turning in the test, individually staple all the work for each problem and place each problem's work in the appropriate folder.

Note that there may be people taking the test after you, so you are not allowed to talk about the test - even to people outside of this class - until I send along the OK. This includes talking about the specific problem types, how long it took you, how hard you thought it was - really anything. Please maintain the integrity of this test.

You may use the || symbol for resistances in parallel and do not need to expand that construction. Be clear with your use of parentheses, however; simply writing something like

$$R_{eq} = R_1 + R_2 \parallel R_3 + R_4$$

is too vague since it could refer to any of the four combinations below:



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Problem I: [20 pts.] Digital Logic

(1) Given the following logical function:

$$\mathbf{f}(A, B, C, D) = \overline{A}\overline{B}\overline{C}\overline{D} + A\overline{B}\overline{C}D + A\overline{B}C\overline{D} + A\overline{B}C\overline{D} + A\overline{B}\overline{C}D + ABCD + A\overline{B}CD + \overline{A}\overline{B}C\overline{D}$$

- (a) Write the logical function in the compact sum of minterms form (i.e. $\mathbf{f} = \sum_i m_i$) assuming A is the most significant bit and D is the least significant bit.
- (b) Clearly construct a Karnaugh map for the function. Be sure to label it appropriately and completely.
- (c) Determine the Minimum Sum of Products (MSOP) form for the function. Be sure to show your process.
- (d) Finally, determine the Minimum Product of Sums (MPOS) form for the function. Be sure to show your process.

(2) Given the following logical function in MSOP form:

$$\mathbf{g}(J, K, L) = JK + \overline{J}\overline{K} + L$$

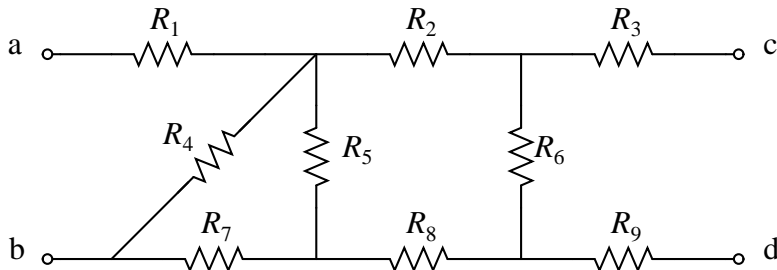
- (a) Write the logical function in the compact sum of minterms form, assuming J is the most significant bit and L is the least significant bit.
- (b) Using standard 2-input logic gates, draw a logic circuit which implements the MSOP form of the expression. Be sure to clearly indicate whether two wires are connected or merely overlapping. Also be sure to draw your gates correctly and neatly!
- (c) For the logic circuit you just drew, determine the complexity. Clearly indicate how you are computing the complexity.

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Problem II: [20 pts.] Equivalentents and Division

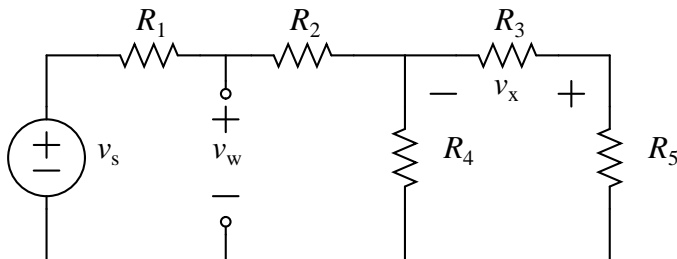
For all parts of this problem, you can *carefully* use the \parallel symbol (and parentheses) as appropriate and do *not* need to simplify expressions using that symbol.

(1) For the following network:



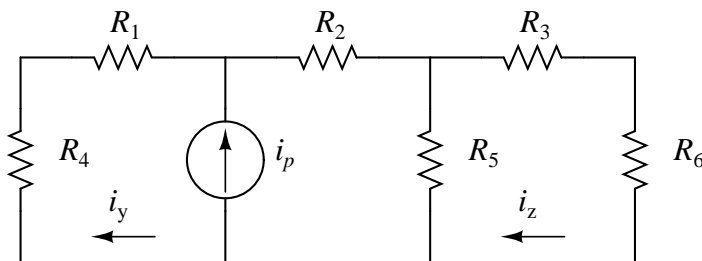
- (a) Find the equivalent resistance as seen from terminals a and b , R_{ab}
- (b) Find the equivalent resistance as seen from terminals c and d , R_{cd} .

(2) For the following circuit:



clearly show voltage division to obtain expressions for v_w and v_x in terms of the resistors and v_s .

(3) For the following circuit:

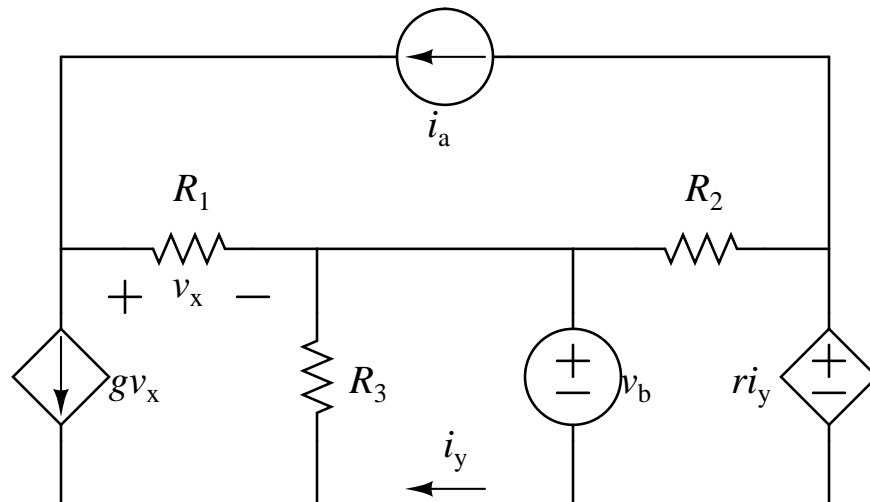


clearly show current division to obtain expressions for i_y and i_z in terms of the resistors and i_p .

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Problem III: [20 pts.] The Basics

Given the following circuit:



and assuming that constants g and r , the values for the passive elements (R_1 , R_2 , R_3), and the values for the independent sources (i_a , v_b) are known, determine expressions for the following items in terms of known values. Note - if you solve for an unknown in terms of known quantities and clearly indicate that expression, you may use that unknown in other calculations without substitution. Clearly indicate the nature and location of any equations you use to solve for an unknown. Put your expressions next to the appropriate bullet below:

- v_x

- i_y

- $p_{\text{abs}, R_2} =$

- $p_{\text{del}, i_a} =$

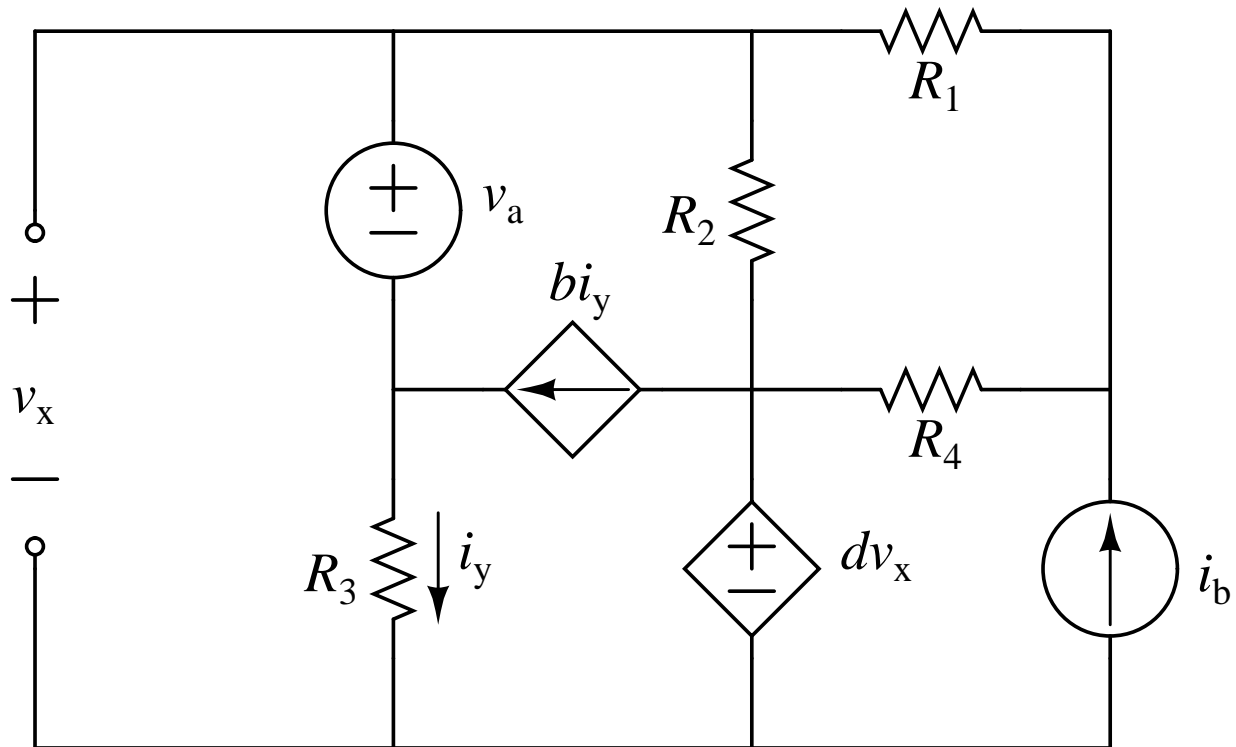
- $p_{\text{del}, \text{CCVS}} =$

- $p_{\text{del}, \text{VCCS}} =$

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Problem IV: [20 pts.] Node Voltage Method

Given the following circuit:



and assuming that constants b and d , the values for the passive elements (R_1 through R_4), and the value for the independent sources (v_a , i_b) are known,

- (1) *Clearly* demonstrate the use of the Node Voltage Method in labeling unknowns for the circuit and in determining a complete set of linearly independent equations that could be used to solve for these unknowns. List the complete set of unknowns you believe your equations will find. Please put the list of unknowns and the equations on a separate piece of paper; you can label the circuit above.
- (2) Assuming you are able to solve for those unknowns, write expressions for

- $p_{\text{abs}, R_2} =$

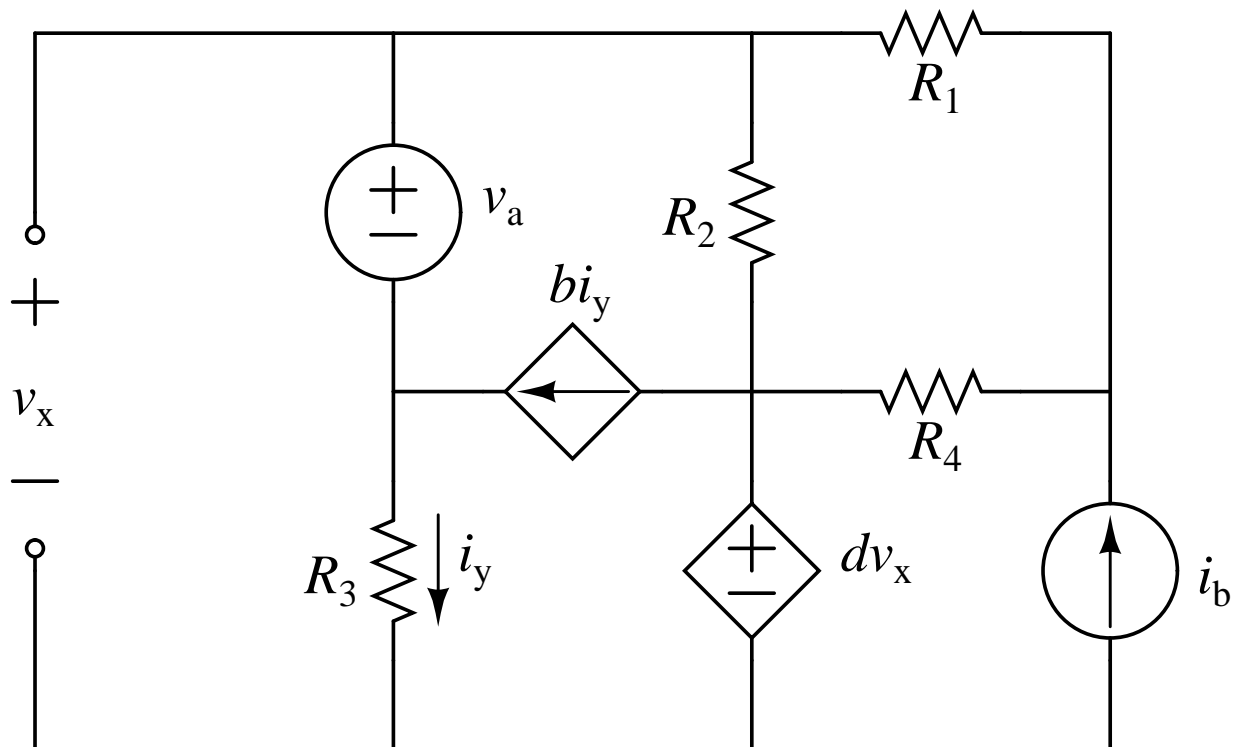
- $p_{\text{del}, \text{VCVS}} =$

- $p_{\text{del}, \text{CCCS}} =$

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Problem V: [20 pts.] Mesh/Branch Current Method

Given the following circuit:



and assuming that constants b and d , the values for the passive elements (R_1 through R_4), and the value for the independent sources (v_a , i_b) are known,

- (1) *Clearly* demonstrate the use of *either* the Mesh Current Method or the Branch Current Method in labeling unknowns for the circuit and in determining a complete set of linearly independent equations that could be used to solve for these unknowns. List the complete set of unknowns you believe your equations will find. Also state whether you believe you are using the MCM or the BCM. Please put the list of unknowns and the equations on a separate piece of paper; you can label the circuit above.
- (2) Assuming you are able to solve for those unknowns, write expressions for

- $p_{\text{abs}, R_2} =$

- $p_{\text{del}, \text{VCVS}} =$

- $p_{\text{del}, \text{CCCS}} =$